CES 2026

AUMOVIO presents the latest version of its central high-performance computer with NXP’s newest vehicle processor

* **Flexible, scalable, powerful: At CES in Las Vegas, AUMOVIO showcases the latest generation of its central vehicle computer powered by the NXP’s S32N79**
* **Together with NXP’s super-integration processor, the high-performance computer delivers safe, modular computing with hardware-enforced function isolation, enabling OEMs to securely consolidate multiple vehicle core functions**
* **Accelerated prototyping due to virtual development environment**
* **AUMOVIO and NXP continue their long-standing collaboration in system and chip development**

Las Vegas, USA. January 6, 2026. For the first time, AUMOVIO will demonstrate the capabilities of the latest generation of its Vehicle Control High-Performance Computer (VC HPC) at CES 2026 in Las Vegas. The system, which bundles functions from areas such as chassis, motion and body, is built on NXP Semiconductors’ new S32N7 super-integration processor series. The S32N79 brings simplicity by consolidating various domains onto a single platform and delivers a secure, modular computing system in which multiple core vehicle functions are hardware-isolated and can reliably operate together. This creates the foundation for updating individual applications in a targeted and independent manner.

“Our goal is to make compute performance in vehicles safer, more efficient, and more flexible,” says Jean-François Tarabbia, Member of the Executive Board and Head of the Architecture and Network Solutions business area at AUMOVIO. “Our VC HPC solution with the integrated new NXP chip provides the technological foundation for vehicles to receive new and enhanced software functions seamlessly throughout their entire lifecycle.”

The latest generation of the VC HPC combines AUMOVIO’s system and software expertise with the performance and safety architecture of NXP’s new S32N79. By virtualizing the entire development and test environment, the technology company was able to create and validate the software in a digital twin long before the physical chip was available. As a result, once the first S32N79 sample was received, AUMOVIO was able to build a fully functional VC HPC prototype in record time.

Integration of multiple vehicle functions

Traditional ECU architectures strictly separate domains and map them to dedicated microcontrollers. In contrast, the new VC HPC integrates these functions centrally on a single chip while still isolating them to avoid redundancies. For example, a safety-critical braking function can run on the same chip as a comfort or chassis module – without the systems influencing one another.

This “cross-domain integration” reduces the number of ECUs, saves weight, and enables a more flexible vehicle architecture. For car manufacturers, this means less complexity, lower costs, and greater potential to deliver software updates or new functions throughout the vehicle’s lifecycle. At the same time, lifetime update and upgrade capability are key to long-term value retention – for both OEMs and end users. The VC HPC can be flexibly customized to customer requirements and integrated into different vehicle architectures, whether domain-based or server-zone-based.

Intelligent data utilization and efficient energy management

The VC HPC with NXP’s S32N79 provides a secure foundation for data-driven vehicle services with optional Edge AI support. It can process sensor information in real time and leverage it for new applications such as predictive maintenance, optimized diagnostics, or additional connected data services that scale across the entire vehicle fleet.

Designed not only for powerful performance but also for efficiency, the VC HPC uses intelligent power modeling to precisely adapt energy consumption to different operating states – whether during charging, in energy-saving standby mode, when parked, or during power-intensive driving.

Joint development of system and chip architecture

AUMOVIO and NXP share a long history of working together in the development of system and chip solutions.  
  
“Our close collaboration with AUMOVIO underscores how crucial it is to consider hardware and software together from the beginning – from system architecture to integration in real vehicle environments,” says Jens Hinrichsen, Executive Vice President and General Manager, Analog and Automotive Embedded Systems at NXP. “Our solutions give OEMs a clear advantage in implementing software-defined vehicle concepts – with a platform that is scalable, safe, secure, energy-efficient, and software-compatible across the entire S32N family.”

AUMOVIO at CES

AUMOVIO will showcase its latest technologies in a private exhibition space in the Central Plaza opposite the Las Vegas Convention Center from Tuesday, January 6, through Friday, January 9. The technology and electronics company offers a wide array of solutions that bring mobility innovations to life. An exclusive, invitation-only media event is scheduled for January 6.

**Images and captions**

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| Ein Bild, das Rad, Fahrzeug, Transport, Landfahrzeug enthält.  KI-generierte Inhalte können fehlerhaft sein.    AUMOVIO\_PP\_Vehicle Control High Performance Computer | AUMOVIO’s Vehicle Control High-Performance Computer with the NXP S32N79 brings together functions from the chassis, motion and body domains within an integrated vehicle architecture. |

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Since its spin-off in September 2025, AUMOVIO continues the business of the former Continental group sector Automotive as an independent company. The technology and electronics company offers a wide-ranging portfolio that makes mobility safe, exciting, connected, and autonomous. This includes sensor solutions, displays, braking, and comfort systems, as well as comprehensive expertise in software, architecture platforms, and assistance systems for software-defined vehicles. In the fiscal year 2024, the business areas, which now belong to AUMOVIO, generated sales of 19.6 billion Euro. The company is headquartered in Frankfurt, Germany and has over 86,000 employees in more than 100 locations worldwide.  